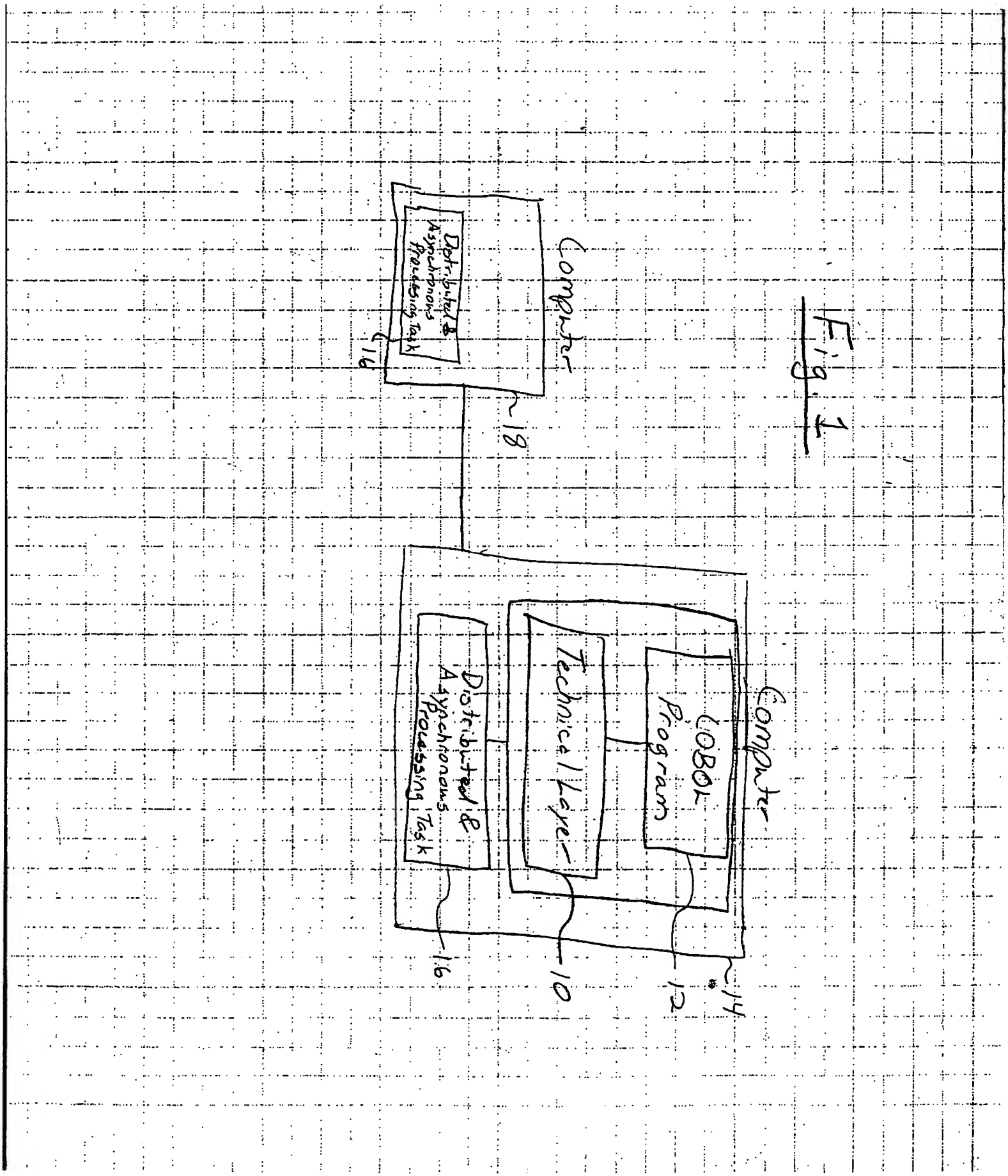
Fig. 1
Computer
Distributed & Asynchronous Processing Task
16
18
Computer
COBOL Program
Technical Layer
Distributed & Asynchronous Processing Task
12
14
10
16

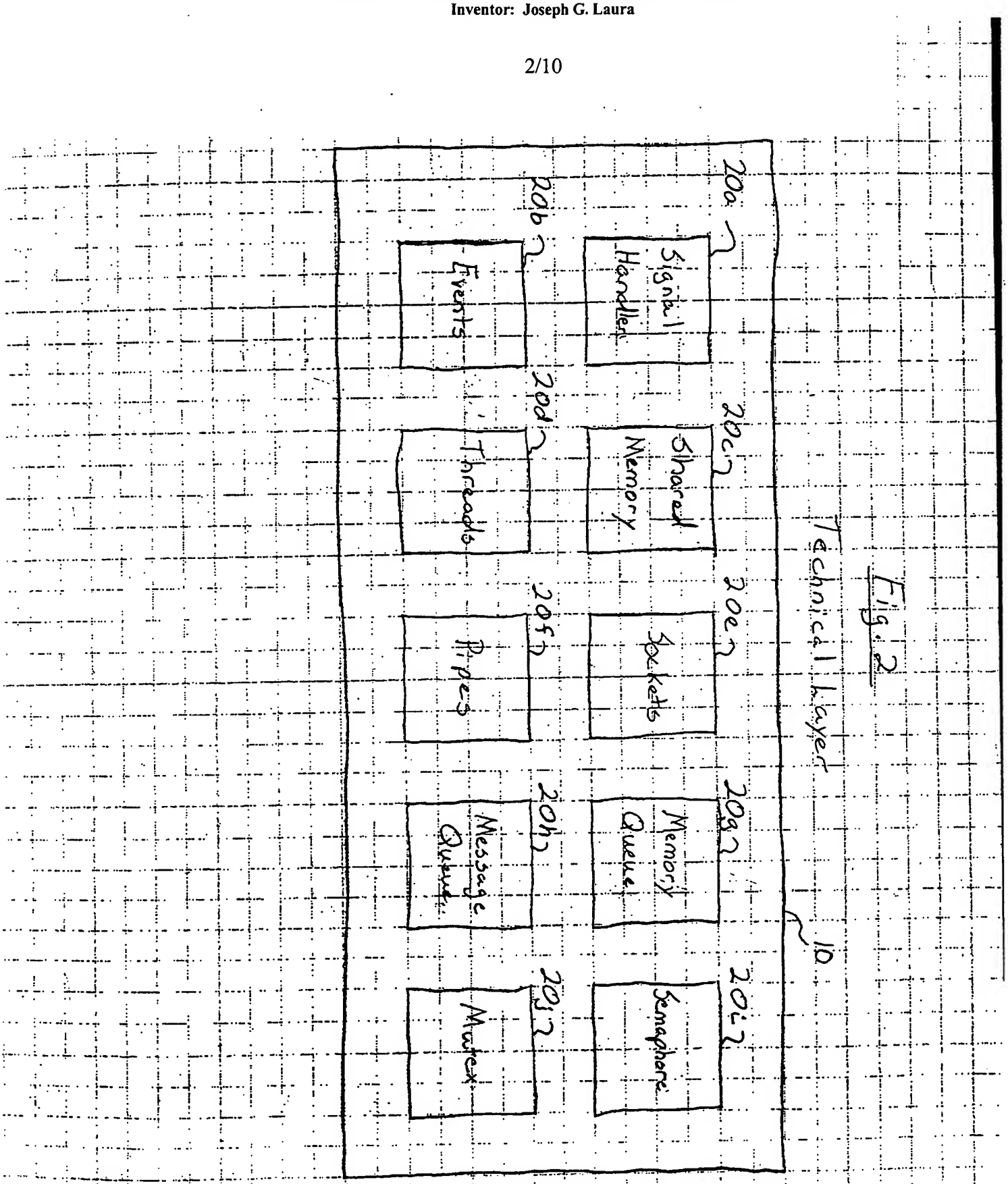
Fig. 2
Technical Layer
10
20a
Signal Handlers
20b
Events
20c
Shared Memory
20d
Threads
20e
Sockets
20f
Pipes
20g
Memory Queues
20h
Message Queues
20i
Semaphore
20j
Mutex

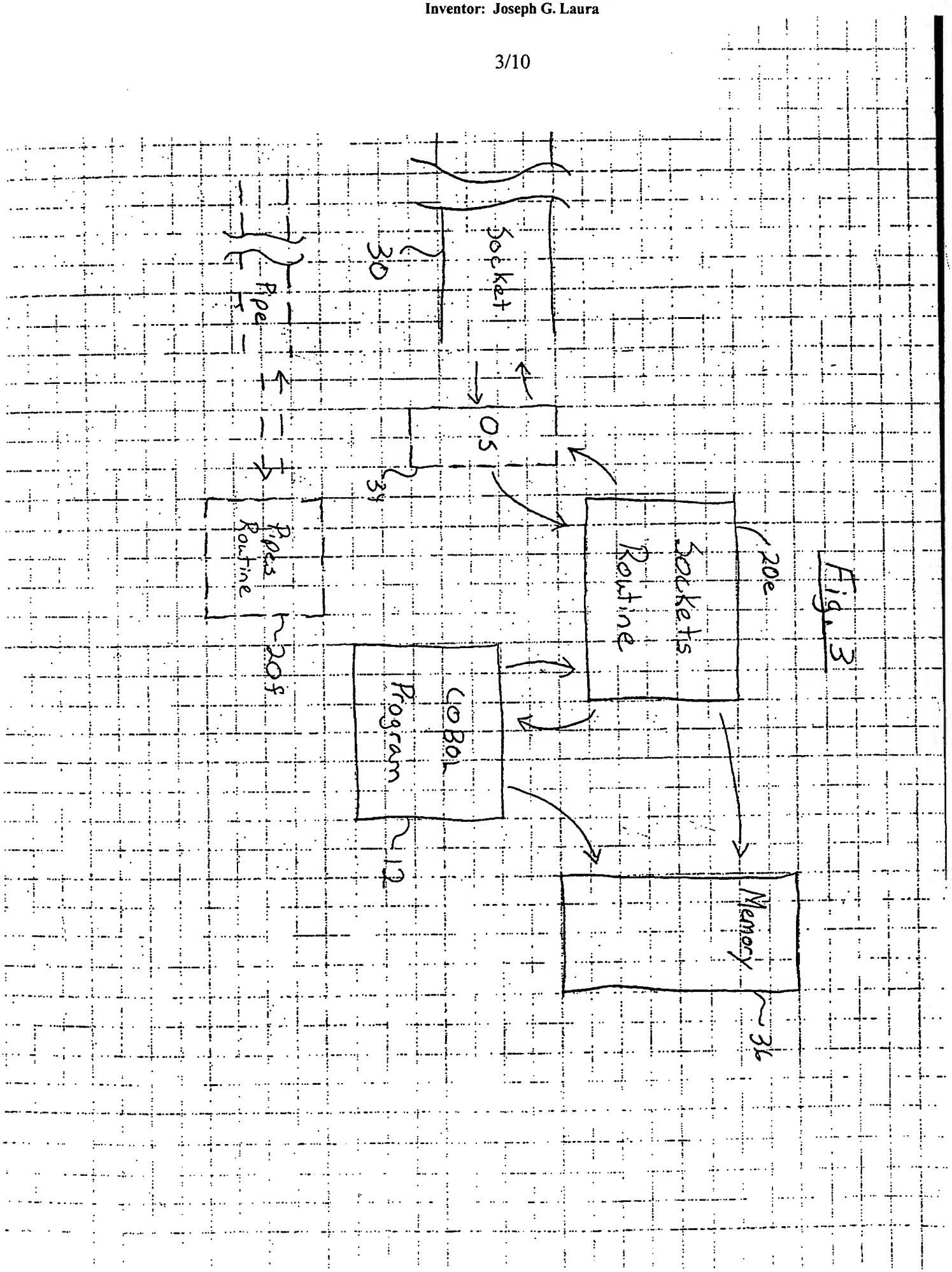
Fig. 3
Sockets Routine
20e
Memory
36
COBOL Program
12
Pipes Routine
20f
Pipe
Socket
30
05
34

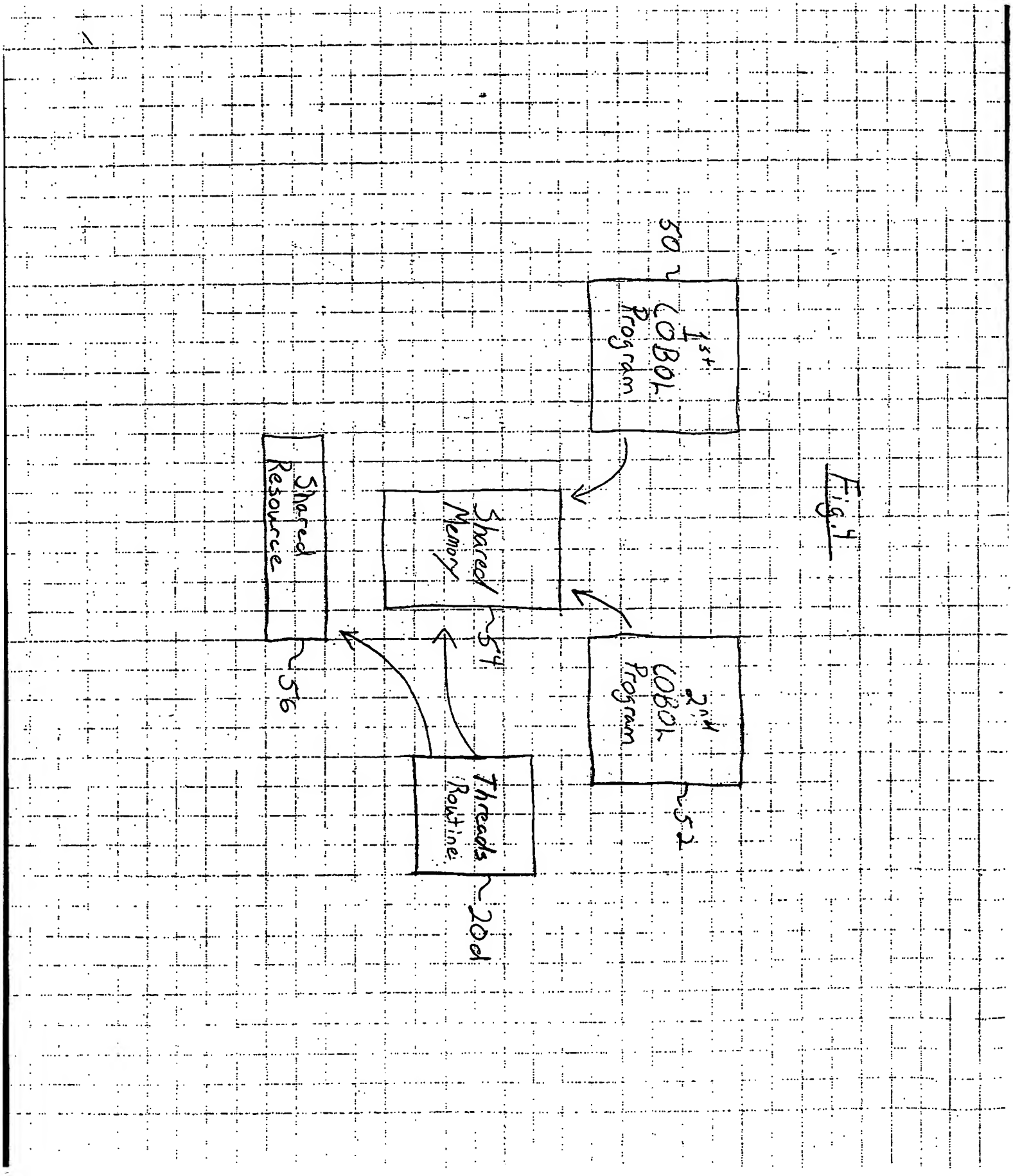
50
1st COBOL Program
Shared Memory
54
Shared Resource
56
2nd COBOL Program
52
Threads Routine
20d

Fig. 4

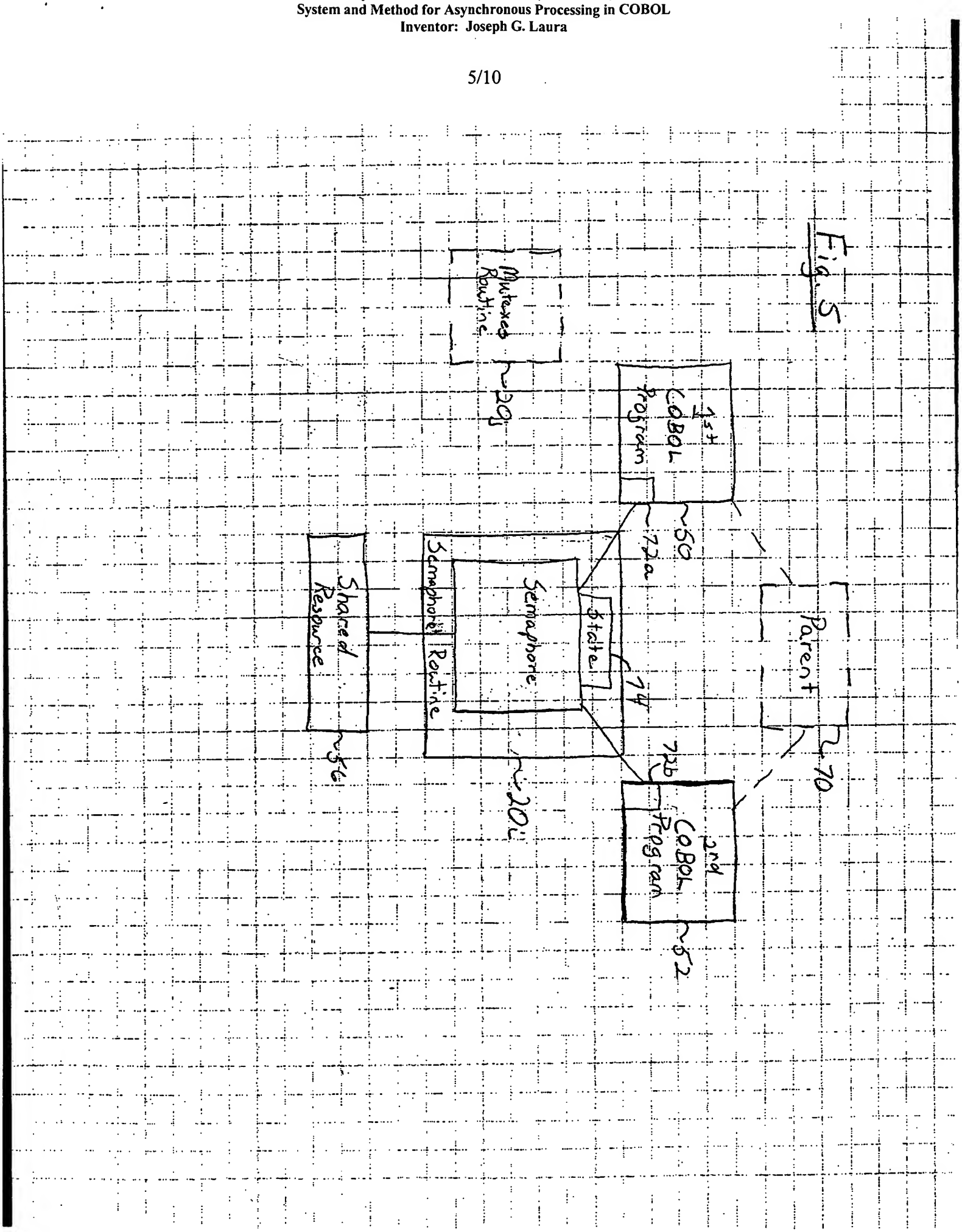
Fig. 5
Parent
70
1st COBOL Program
50
72a
2nd COBOL Program
52
72b
Mutexes Routine
20
State
74
Semaphore
Semaphore Routine
20
Shared Resource
56

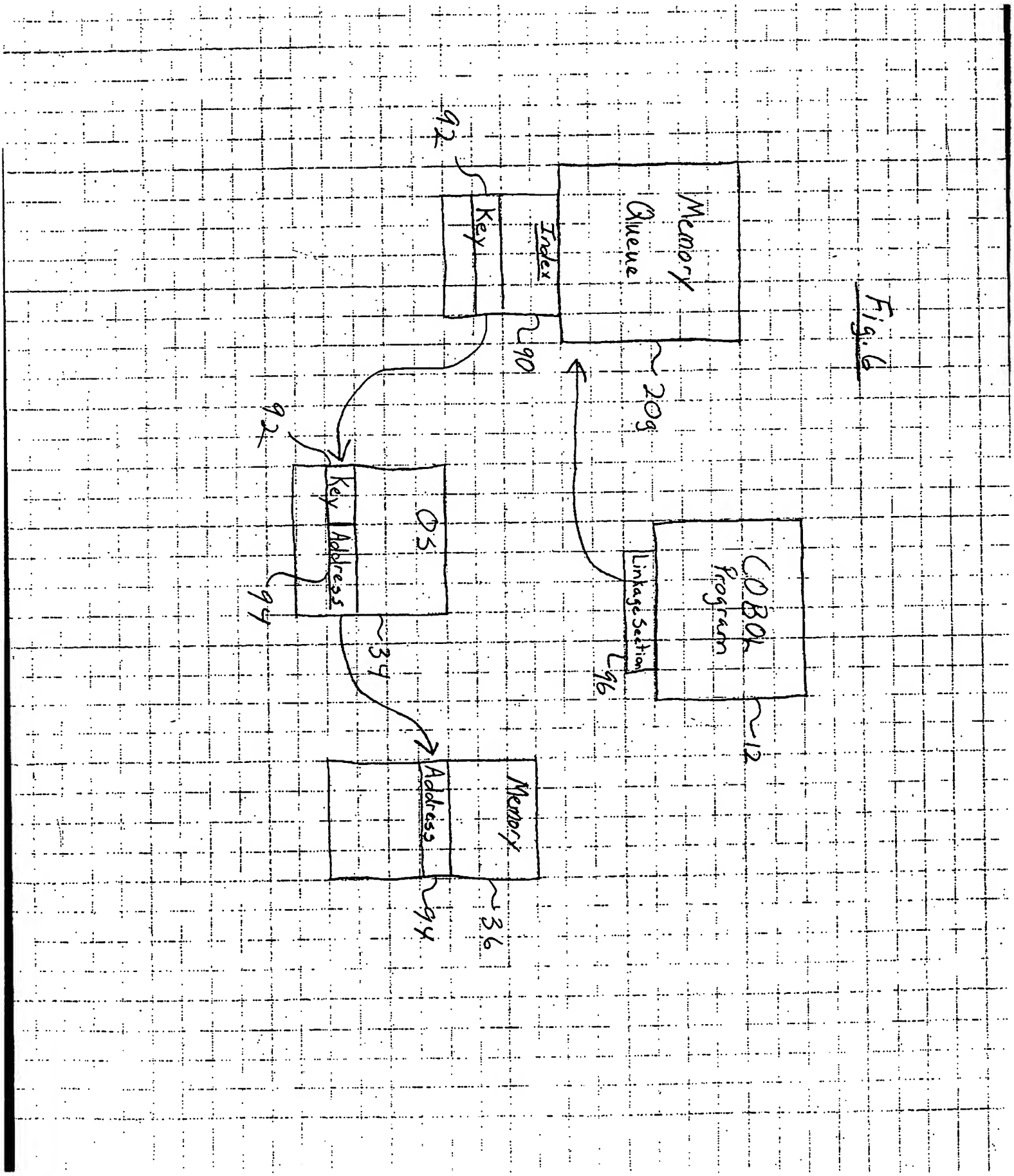
Fig. 6
Memory
Queue
Index
Key
92
90
20
92
Key
Address
94
OS
34
Linkage Section
96
COBOL
Program
12
Address
Memory
94
36

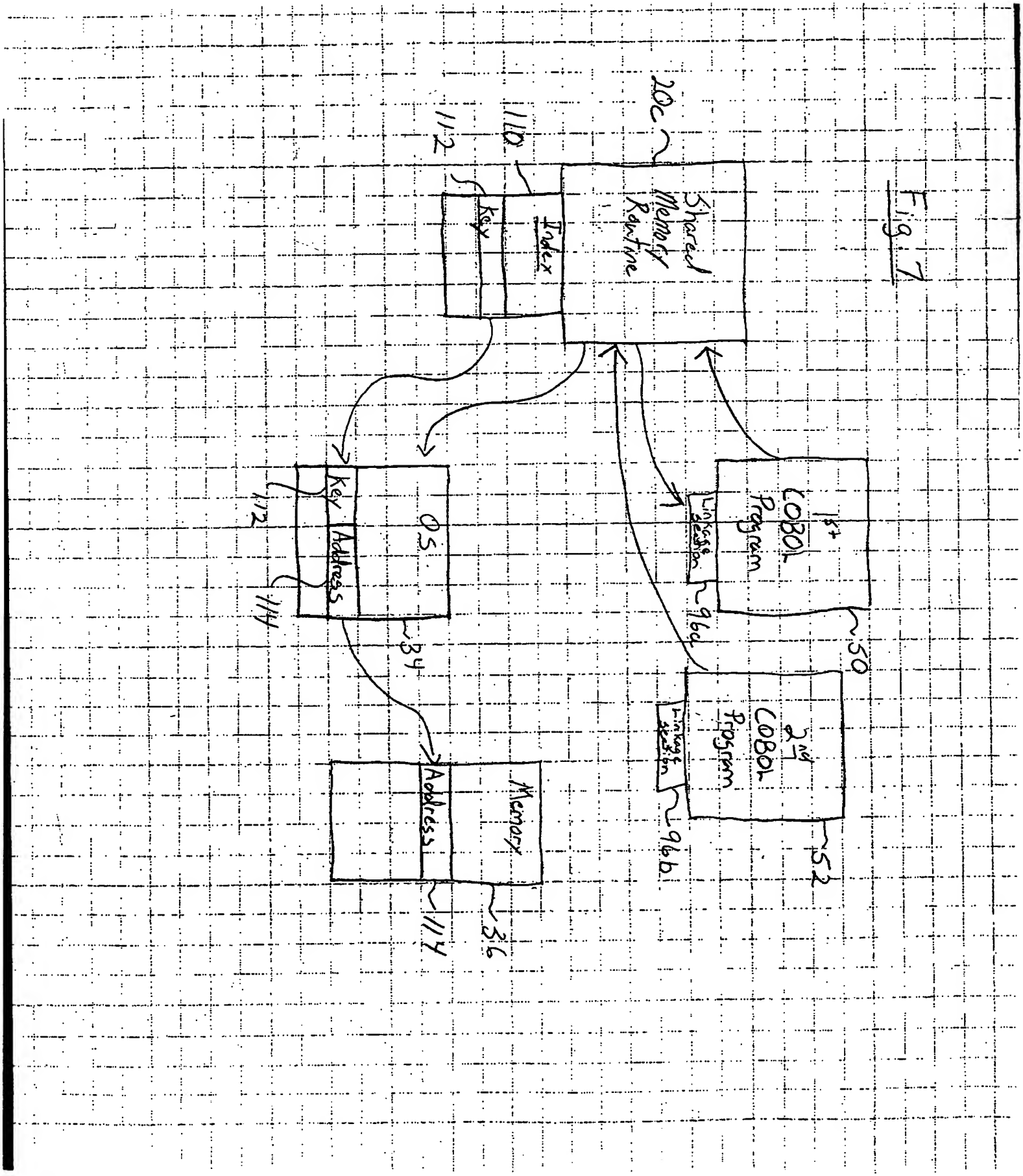
Fig. 7
20c
Shared Memory Routine
110
112
Index
Key
1st COBOL Program
Linkage Section
96a
50
2nd COBOL Program
Linkage Section
96b
52
OS
Key
Address
112
114
34
Address
Memory
36
114

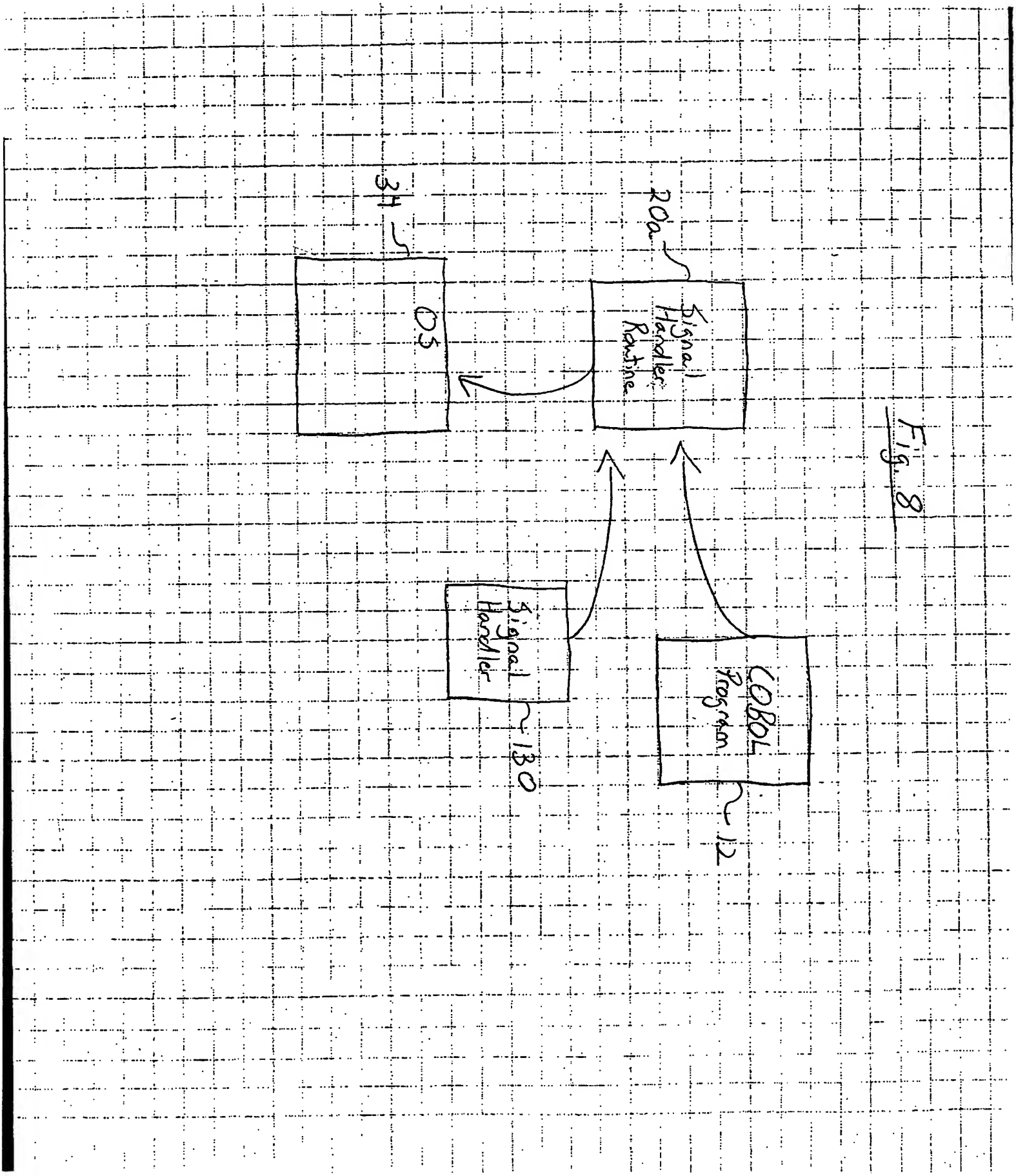
34
05
20a
Signal Handler Routine
Signal Handler
130
COBOL Program
12

Fig. 8

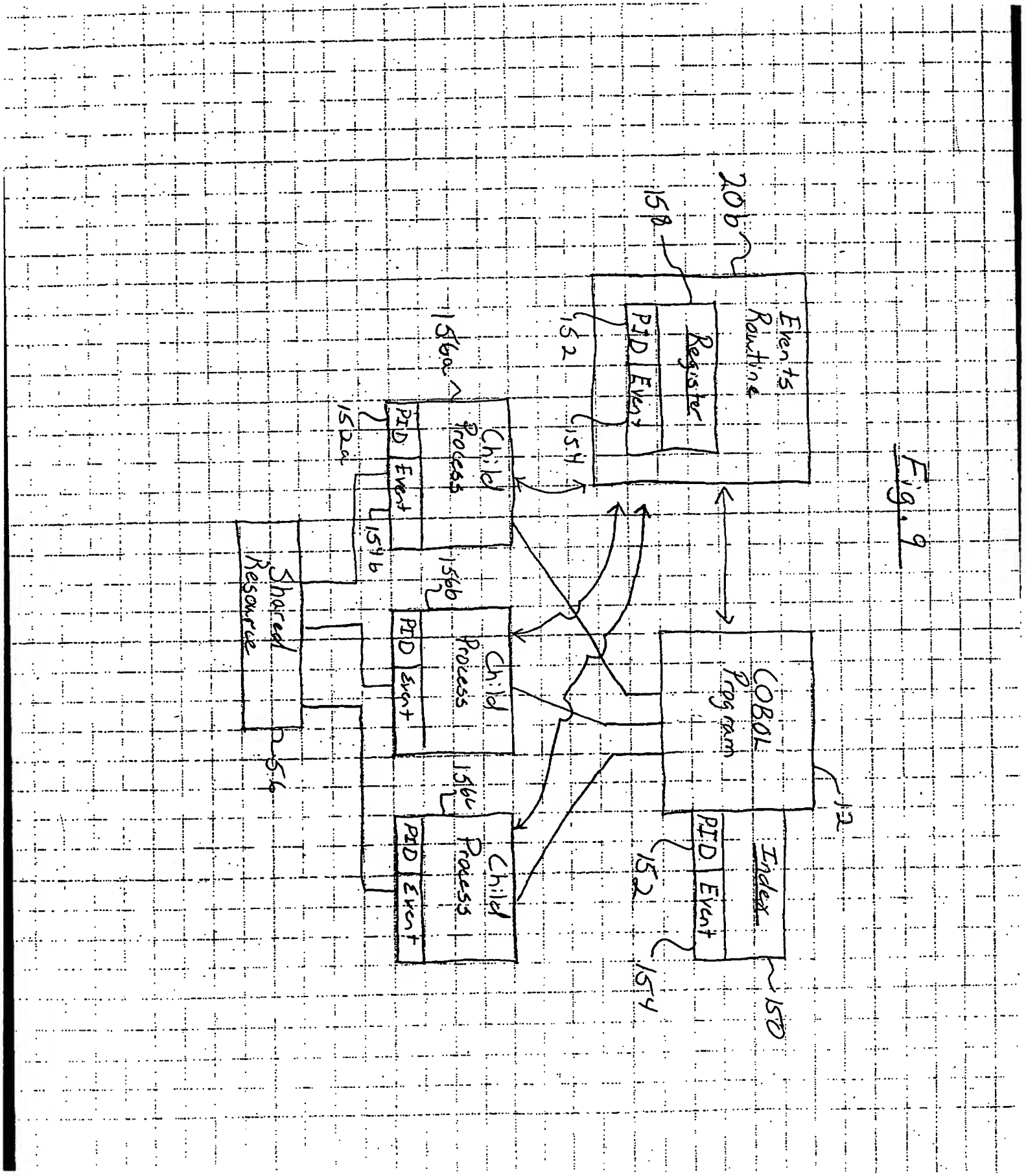
Fig. 9
Events Routine
Register
PID
Event
20b
158
152
154
COBOL Program
Index
PID
Event
152
154
150
12
Child Process
156a
PID
Event
152a
154a
156b
Child Process
PID
Event
156c
Child Process
PID
Event
Shared Resource
56

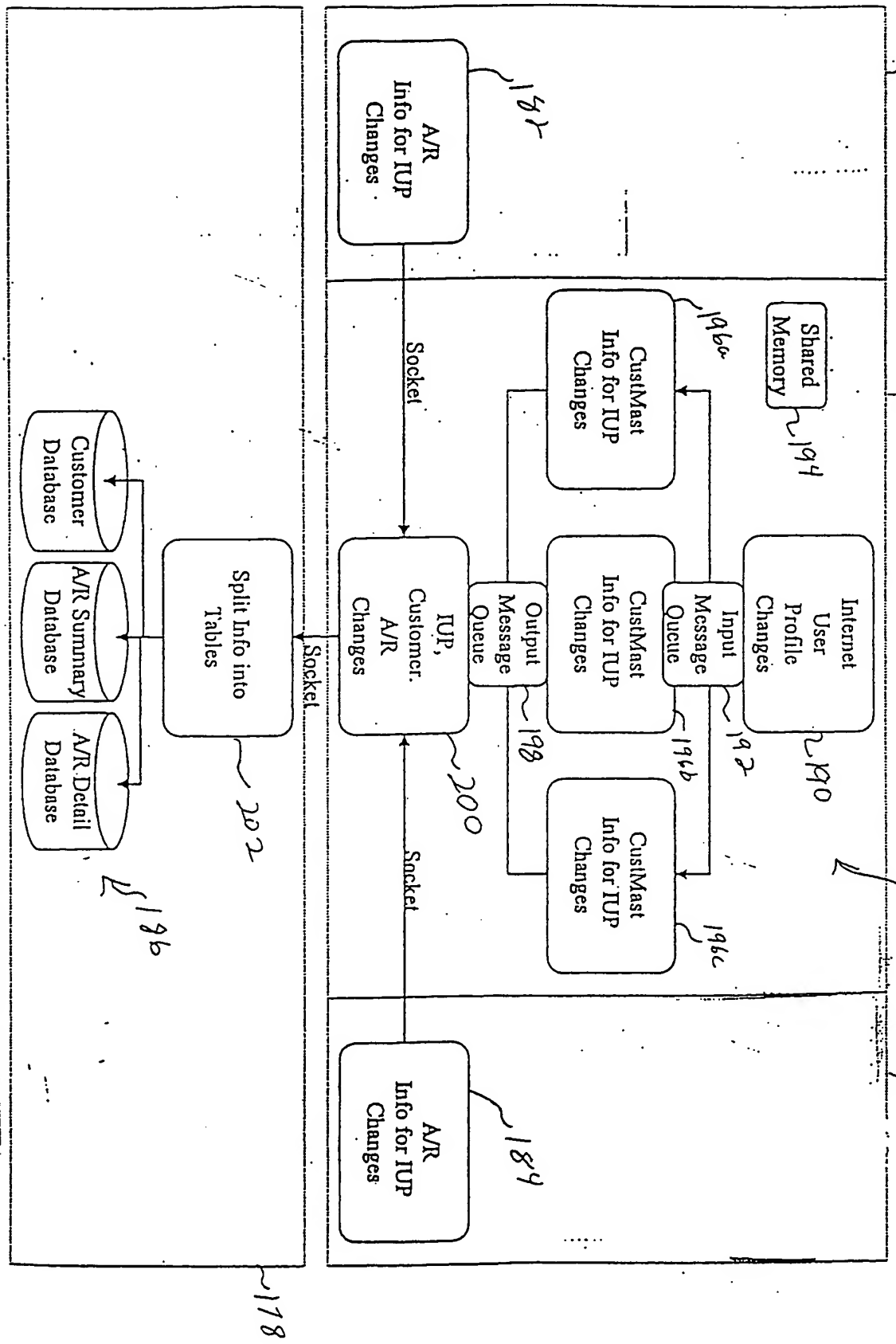
170
172
174
176
178
180
182
184
186
190
192
194
196a
196b
196c
198
200
202
A/R Info for IUP Changes
Socket
CustMast Info for IUP Changes
Shared Memory
Internet User Profile Changes
Input Message Queue
Output Message Queue
IUP, Customer, A/R Changes
Split Info into Tables
Customer Database
A/R Summary Database
A/R Detail Database

Fig. 10